NP001 PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Number: 7,082,178

Issue Date: July 25, 2006
Name of Patentee: David Meltzer

Application No.: 10/016,915

Filing Date: December 14, 2001

Confirmation No.: 9628

Title of Invention: A Lock Detector Circuit for Dejitter Phase Lock Loop (PLL)

Applications

REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT FOR PTO MISTAKE (37 CFR §1.322(a))

Sir:

It is noted that an error appears or errors appear in this patent of a clerical, typographical or minor nature or character, as more fully described below, due to a mistake by the U.S. Patent and Trademark Office.

Attached hereto is Form PTO-1050.

The exact page(s) and line number(s) where the error(s) occur in the patent are:

Title Page,

Item [54], change "LOCK DETECTOR CIRCUIT FOR DEJITTER PHASE LOCK LOOP (PLL)" to -- LOCK DETECTOR CIRCUIT FOR DEJITTER PHASE LOCK LOOP (PLL) APPLICATIONS--.

Column 10,

Line 19, change "said chase" to --said phase--.

Patentee's undersigned attorney may be reached at the telephone number listed below. All correspondence should continue to be directed to our address listed below.

Respectfully submitted,

/Rosalio Haro/ Rosalio Haro Registration No. 42,633

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Date: October 10, 2006

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page 1 of 1

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ISSUE DATE: July 25, 2006
INVENTOR(S): David Meltzer

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

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Column 10,

Line	19	change	"caid	chase"	to	hies	nhace
ше	TĐ,	change	Saru	chase	w	saiu	phase

MAILING ADDRESS OF SENDER:

Epson Research and Development, Inc. Intellectual Property Department 150 River Oaks Parkway, Suite 225 San Jose, CA 95134